

#17

11-26-01

B. Hilliard

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Aguro
Serial No.: 09/281,042
Filed: 03/30/1999
Title: COMPUTER SYSTEM

Docket No.: TIJ-26495
Art Unit: 2123
Examiner: Jones, H.

LETTER

Assistant Commissioner for Patents
Washington, DC 20231

Sir:

CERTIFICATION OF FACSIMILE
TRANSMISSION

I hereby certify that the following papers are
being transmitted by facsimile to the U.S. Patent
and Trademark Office at (703) 746-7238 on the
date shown below:

Phyllis Ewing 11-19-01
Phyllis Ewing Date

Enclosed is a copy of the "Marked-up version to show changes" which was included as the last
page of our response mailed October 1, 2001.

I hereby certify that the attached is a true copy from our file.

It is respectfully submitted that the changes made to claims 4 and 8 should be entered for the
purposes of an Appeal because correcting typographical errors places the claims in better form for an
Appeal.

Respectfully submitted,



William B. Kempler
Senior Corporate Patent Counsel
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TIJ-26495

- PAGE 1 -

Marked-up version to show changes:

4. ~~(new)~~ (Amended) An integrated circuit computer system comprising:
a processor interconnected with memory and peripheral circuits on said integrated circuit; ;
a scan-path interface circuit for reading out contents of a predetermined memory or register in said system;
a switching circuit coupled to said processor and to said scan-path interface circuit for switching said scan-path interface circuit between a first mode in which it is enabled and a second mode in which it is disabled; and
security means comprising:
a plurality of input ports for said processor;
a program stored in said memory to operate said ~~process~~ processor to receive a plurality of commands applied to said plurality of input ports and to process said commands to produce a password which is compared with a predetermined password;
and wherein said switching circuit is responsive to said comparison.

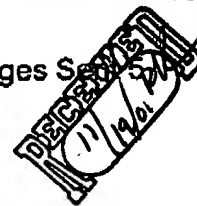
8. ~~(new)~~ (Amended) In an integrated circuit computer system having a processor interconnected with memory and peripheral circuits on said integrated circuit and coupled to a scan-path interface circuit, a security system comprising:
a plurality of input ports for said processor;
a program stored in said memory to operate said processor to ~~received~~ receive a plurality of commands applied to said plurality of input ports and to process said commands to produce a password which is compared with a predetermined password.

TIJ-26495

To: Technology Center 2100
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Total Pages 5

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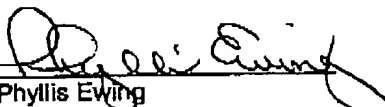
**Official****IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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11/19/2001
Date

FACSIMILE COVER SHEET

<input checked="" type="checkbox"/> FAX COVER SHEET	<input type="checkbox"/> AMENDMENT
<input type="checkbox"/> NEW APPLICATION	<input checked="" type="checkbox"/> EOT 2 mo
<input type="checkbox"/> DECLARATION	<input checked="" type="checkbox"/> NOTICE OF APPEAL
<input type="checkbox"/> ASSIGNMENT	<input type="checkbox"/> APPEAL
<input type="checkbox"/> FORMAL DRAWINGS	<input type="checkbox"/> ISSUE FEE
<input type="checkbox"/> INFORMAL DRAWINGS	<input type="checkbox"/> REPLY BRIEF (IN TRIPLICATE)
<input type="checkbox"/> CONTINUATION APP'N	<input checked="" type="checkbox"/> COPY OF RESPONSE TO FINAL REJECTION
<input type="checkbox"/> DIVISIONAL APP'N	
NAME OF INVENTOR(S): Aguro	
TITLE OF INVENTION: Computer System	
TI FILE NO.: TI-26495	DEPOSIT ACCT. NO.: 20-0668
RECEIPT DATE & SERIAL NO.: Serial No.: 09/281,042 Filing Date: 03/30/1999	
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